a liquid crystal material disposed between said pair of opposed substrates;

a resin adhesive layer formed on said one of the substrates;

a driver circuit comprising thin film transistors that are formed from a substrate separate from said substrates and are adhered to said one of the substrates[,

wherein said thin film transistors are adhered to said one of the substrates] by [a] said resin adhesive layer.

2. (Amended) A liquid crystal display device according to claim 1 wherein said each of thin film transistors has a channel region [comprises] comprising crystal silicon.

7. (Amended) A liquid crystal display device comprising:

a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a driver circuit comprising thin film transistors formed from a substrate separate from [on] said one of the substrates and adhered to said one of the substrates by a resin; and

a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit, [wherein said thin film transistors are adhered to said one of the substrates by a resin,] and said contact hole has a tapered configuration.

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8. (Amended) A liquid crystal display device according to claim 7 wherein said each of

thin film transistors has a channel region [comprises] comprising crystal silicon.

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15. (Amended) A liquid crystal display device comprising:

a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a driver circuit comprising thin film transistors formed from a substrate separate from [on] said one of the substrates and adhered to said one of the substrates by a resin layer; and

a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit, wherein said passivation film comprises at least two layers having different etching rates, and said contact hole has a tapered configuration.

17. (Amended) A liquid crystal display device according to claim 15 wherein said each of thin film transistors has a channel region [comprises] comprising crystal silicon.

22. (Amended) A liquid crystal display device comprising:

a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a driver circuit comprising thin film transistors formed [on] from a substrate separate from said one of the substrates[, wherein said thin film transistors are] and adhered to said one of the substrates by a resin, [and] wherein said driver circuit is electrically [connected] coupled to said pixel circuit through a metal bump.

Please add the following new claims:

--27. (New) A liquid crystal display device according to claim 1 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

28. (New) A liquid crystal display device according to claim 1 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

29. (New) A liquid crystal display device according to claim 7 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

30. (New) A liquid crystal display device according to claim 7 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

31. (New) A liquid crystal display device according to claim 15 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

32. (New) A liquid crystal display device according to claim 15 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

33. (New) A liquid crystal display device according to claim 22 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

34. (New) A liquid crystal display device according to claim 22 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

35. (New) A liquid crystal display device according to claim 22 wherein each of said thin film transistors has a channel region comprising crystal silicon.

36. (New) A display device comprising:

a substrate;

a pixel circuit formed over said substrate for switching pixels of said display device,

a driver circuit comprising thin film transistors formed over said substrate, and

a resin, adhering said thin film transistors to said substrate.

37. (New) A display device according to claim 36 wherein said each of thin film transistors has a channel region comprising crystal silicon.

38. (New) A display device according to claim 36 wherein said substrate comprises a plastic.

39. (New) A display device according to claim 36 wherein said driver circuit is covered by another substrate opposed to said substrate.

40. (New) A display device according to claim 36 wherein said device is a passive type.

41. (New) A display device according to claim 36 wherein said device is an active matrix type.

42. (New) A display device according to claim 36 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

43. (New) A display device according to claim 36 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

44. (New) A display device comprising:

a substrate;

a pixel circuit formed over said substrate for switching pixels of said display device a driver circuit comprising thin film transistors formed over said substrate; and

a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit, wherein said thin film transistors are adhered to said substrate by a resin, and said contact hole has a tapered configuration.

45. (New) A display device according to claim 44 wherein said passivation film comprises at least two layers having different etching rates, and said contact hole has a tapered configuration.

46. (New) A display device according to claim 44 wherein said each of thin film transistors has a channel region comprising crystal silicon.

47. (New) A display device according to claim 44 wherein said substrate comprises a plastic.

48. (New) A display device according to claim 44 wherein said driver circuit is overlapped by another substrate opposed to said substrate.

49. (New) A display device according to claim 44 wherein said device is a passive type.

50. (New) A display device according to claim 44 wherein said device is an active matrix type.

51. (New) A display device according to claim 44 wherein said passivation film comprises polyimide.

52. (New) A display device according to claim 44 wherein said passivation film comprises silicon oxide.

53. (New) A display device according to claim 44 wherein said pixel circuit comprises a first plurality of transparent conductive films and a second plurality of transparent conductive films extending across said first plurality of transparent conductive films.

54. (New) A display device according to claim 44 wherein said pixel circuit comprises pixel electrodes connected to TFTs.

55. (New) A display device comprising:

a substrate;

a pixel circuit for switching pixels of said display device,

a driver circuit comprising thin film transistors formed over said substrate, a resin, adhering said thin film transistors

to said substrate and a metal bump electrically coupling said driver circuit to said pixel circuit.

56. (New) A display device according to claim 55 wherein said each of thin film transistors has a channel region comprising crystal silicon.

57. (New) A display device according to claim 55 wherein said substrate comprises a plastic.

58. (New) A display device according to claim 55 wherein said driver circuit is covered by another substrate opposed to said substrate.

59. (New) A display device according to claim 55 wherein said device is a passive type.

60. (New) A display device according to claim 55 wherein said device is an active matrix type.